# TENTAMEN / EXAM

## TDDC78

## Programmering av parallelldatorer / Programming of parallel computers

## 2015-08-18, 14:00–18:00 G32

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**Hjälpmedel / Allowed aids:** Engelsk ordbok / dictionary from English to your native language

**Examinator:** Christoph Kessler

**Jourhavande lärare:**
Christoph Kessler (IDA) 013-28-2406; 0703-666687; visiting at ca. 15:30;
Lu Li (course assistant) 0704-759692, after 15:30

**Maximalt antal poäng / Max. #points:** 40

**Betyg / Grading (prel.):** The preliminary threshold for passing (grade 3) is at 20p, for grade 4 at 28p, for grade 5 at 34p.
Because of new regulations by Linköping University, we can no longer give ECTS grades. If you need one, please contact the course secretary after the result has been entered in LADOK.

**Tentavisning / Exam review:** none for re-exams. Exams will be archived in the IDA student expedition.

## General instructions

- Please use a new sheet of paper for each assignment. Order your sheets by assignments, number them, and mark them on top with your exam ID number and the course code.
- You may answer in either English or Swedish. **English is preferred** because not all correcting assistants understand Swedish.
- Write clearly. Unreadable text will be ignored.
- Be precise in your statements. Unprecise formulations may lead to a reduction of points.
- Motivate clearly all statements and reasoning.
- Explain calculations and solution procedures.
- The assignments are *not* ordered according to difficulty.

1. (6 p.) **Performance tools and analysis**

   (a) (1 p.) Which performance data collection method is required in order to be able to draw a processor-time diagram (also known as a *Gantt chart*)? Justify your answer (technical reasons).

   (b) (1p) Modern tool-suites for performance analysis of parallel programs consist of a collection of several kinds of tools. Give four different kinds of such tools. *(I.e., no concrete tool names, but a short term saying what each kind of tool does.)*

   (c) (1.5p) Which property/properties of real parallel computers is/are modeled well by the BSP (Bulk-Synchronous Parallel) model, and which property / properties of real parallel computers does it abstract from?

   (d) (1p) When is a parallel algorithm for some problem (asymptotically) *cost-optimal*? *(give a formal definition)*

   (e) (1.5p) What is the difference between relative and absolute parallel speed-up? Which of these is expected to be higher, and why?

2. (2 p.) **Parallel programming models**

   Explain the parallel program execution styles *fork-join* and *SPMD*, and explain the main difference. (1p)

   Which one is more comfortable for the programmer, and why? (0.5p)

   Which one is used in MPI? (0.5p)

3. (4 p.) **Parallel program design methodology**

   Foster's design methodology consists of four stages. Name and explain them. Give details about their goals. What are the important properties of the result of each stage? Be thorough!

4. (5 p.) **Parallel computer architecture**

   (a) Given an application with a fixed performance requirement (in GFlops). Why can, provided that the application can be parallelized (work-)efficiently, the transition from a single-core to a multicore execution platform be advantageous from a power efficiency point of view? (1p)

   (b) How does the MSI *write-invalidate protocol* for cache coherence in a bus-based shared memory system work? *(Be thorough)* (2p)

   (c) For a shared memory architecture, does sequential memory consistency imply a deterministic total ordering of all memory accesses? Justify your answer. (1p)

   (d) Give an example of a scalable interconnection network topology (name and sketch) where the node degree is constant, i.e. independent of the number of nodes. What is the advantage of a constant node degree? (1p)

5. (5 p.) **OpenMP**

(a) Most OpenMP work-sharing constructs, such as the parallel for/do loops, allow to specify an optional `nowait` clause. For example,

```
#pragma omp for nowait
for (i=0; i<N; i++)
  { ... }
#pragma omp for
for (j=0; j<M; j++)
  { ... }
```

(i) Describe the effect of the `nowait` clause on the computation of the executing processors. (0.5p)

(ii) Why can it be beneficial for performance to use `nowait`? (0.5p)

(iii) Formulate a sufficient condition (dependence-based argument) on the two loops in the example above for when it is safe to use `nowait` in the first of the two loops. (1p)

(b) OpenMP provides different scheduling methods for parallel loops. Characterize the kind of loops that are expected to perform best with *static scheduling*, and explain why. (1.5p)

(c) What is the memory consistency model guaranteed by OpenMP implementations? (short answer) (0.5p)

(d) Why is the design of OpenMP helpful for *incremental* parallelization of sequential codes? (1p)

6. (8 p.) **Parallel Basic Linear Algebra**

(a) (3p) The BLAS (Basic Linear Algebra Subroutines) library API comes in three different levels (BLAS Level 1, 2, 3).

(i) Name one representative function from each level and give a short description. (1.5p)

(ii) Why is it very important for a supercomputing center to have efficient implementations of BLAS installed? (0.5p)

(iii) Which of the levels is most likely to have efficient parallel implementations even where interprocessor communication is expensive, and why? (1p)

(b) (5p) Consider the following straightforward sequential code for the multiplication of two $N \times N$ square matrices $A$ and $B$:

```
for (i=0; i<N; i++)
   for (j=0; j<N; j++)
      C[i][j] = 0.0;
for (i=0; i<N; i++)
   for (j=0; j<N; j++)
      for (k=0; k<N; k++)
         C[i][j] = C[i][j] + A[i][k] * B[k][j];
```

(i) Apply *tiling* with tile size $S \times S$ (with $1 < S \ll N$) to the second loop nest. Show the resulting pseudocode. (1p)

(ii) Why is tiling (with a suitably chosen $S$) beneficial for the performance of this code on modern processor architectures? (1p)

(iii) Assume that your processors offer SIMD instructions that can load, store, multiply or add four floatingpoint words together in one clock cycle if they are adjacent in memory resp. in (SIMD) registers. Which loop in your tiled loop nest is the most suitable one for vectorization using SIMD instructions, and why? Suggest a preparing code transformation that helps with efficient vectorization, and show the resulting pseudocode. (2p)

(iv) Now parallelize the tiled code for a shared-memory parallel system, using OpenMP loop parallelization. Choose a suitable loop to parallelize. If necessary, transform the code. Show the resulting pseudocode. (1p)

7. (4 p.) **Parallel Solving of Linear Equation Systems**

(a) (4p) Consider the problem of solving a triangular linear equation system, e.g. a system $Ux = b$ where $U$ is a $n \times n$ upper triangular matrix (i.e., all elements below the main diagonal are zero) by *backward substitution*, with the following sequential pseudocode:

$x_{n-1} \leftarrow b_{n-1}/u_{n-1,n-1}$;
**for** $i = n-2$ **downto** $0$ **do**
$$x_i \leftarrow \left(b_i - \sum_{j=i+1}^{n-1} u_{i,j}x_j\right)/u_{i,i};$$

i. Derive the asymptotic execution time of the above sequential algorithm as an expression in $n$. (0.5p)

ii. Suggest a possible parallelization for a shared memory system. What kind(s) of parallelism can you exploit here? Show the resulting pseudocode (using e.g. OpenMP or pthreads), and derive the parallel execution time as an expression in $n$ and $p$. You can ignore cache issues for simplicity.
Calculate the parallel efficiency, and discuss possible performance issues where the number $p$ of parallel threads is large. (3.5p)

8. (2 p.) **Transformation and Parallelization of Sequential Loops**

Given the following C loop:

```
a[0] = b[0];
for (i=1; i<N; i++) {
   a[i] = a[i-1] + b[i];
}
```

(Assume for simplicity that the arrays `a`, `b` do not overlap in memory.)

(a) Explain why the loop iterations cannot be executed in parallel in this form (dependence-based argument). (0.5p)

(b) What kind of computation does this loop actually do? (0.5p)

Suggest a *parallel algorithm* for the same problem that could utilize up to $N$ processors in parallel (give the algorithm's name, basic idea and asymptotic parallel time complexity, but no details). (1p)

9. (4 p.) **MPI**

(a) (2 p.) Explain the Communicator concept in MPI. How does it support the construction of parallel software components?

(b) (2 p.) *One-sided communication in MPI*

i. Explain the principle of one-sided communication in MPI-2. (1.5p)
*Hint: You might want to illustrate your answer with a pseudocode example and/or an annotated picture.*

ii. Why is one-sided communication considered being "closer" to the shared-memory programming model than ordinary two-sided message passing? (0.5p)