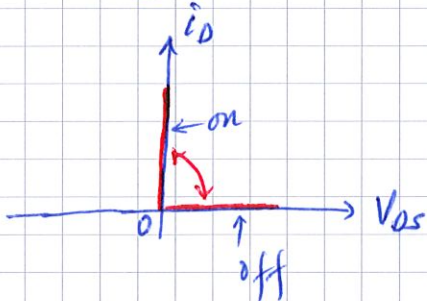


(a) their main difference depends on the forward voltage drop, reverse recovery time and the blocking voltage.

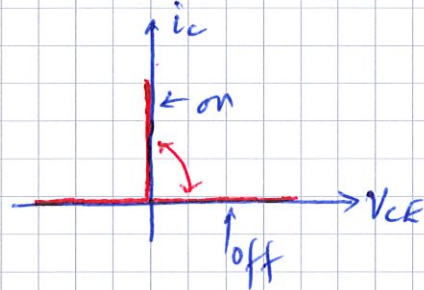
- Schottky diode: low voltage drop, fast recovery, low reverse blocking
- Rectifying diode: low voltage drop, large recovery, high reverse blocking
- switching diodes: high voltage drop, low recovery (2 pts)

(b) MOSFET



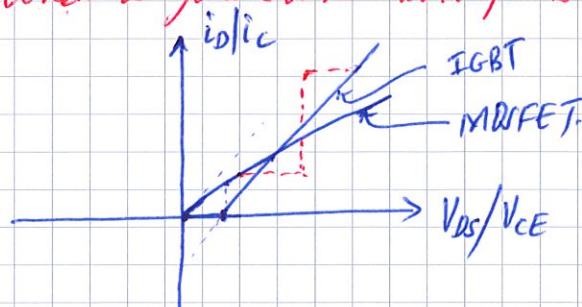
ideal characteristic

IGBT



ideal characteristic

- one difference is reverse blocking for the IGBT when a gate-emitter voltage is not applied.
- Another difference using the simplified i-v characteristics is the absence of a minimum forward voltage for the MOSFET to start conducting when a gate-source voltage is applied.



(2 pts)

(c) the main purpose of an inductor in DC/DC converters is to provide a temporary energy storage given by $W = \frac{1}{2} L i_{max}^2 = \frac{1}{2} L i_{sat}^2$.

If an air gap is used in the inductor design, the inductance value decreases as given by $L = \frac{N^2}{R_c + R_g}$ ← reluctance of air gap.

For a given saturated (maximum) flux density in the core, the saturating current is given from Ampere law as

$$\begin{aligned}
 N \cdot i_{\text{sat}} &= \frac{\Phi}{\mu_0} \cdot R_{\text{tot}} \\
 &= B_{\text{sat}} \cdot A_c \cdot (R_g + R_c) \\
 \Rightarrow i_{\text{sat}} &= \frac{B_{\text{sat}} \cdot A_c \cdot (R_c + R_g)}{N}
 \end{aligned}$$

air gap increases saturating current.

* using ① + ②, it is possible to show that $w = \frac{1}{2} L \cdot i_{\text{sat}}^2$ increases when using an air gap.

(2 pts)

$$\begin{aligned} V_o &= 12 \text{ V} \\ P_o &= 48 \text{ W} \\ V_d &= 20 \text{ V} \\ f_{sw} &= 20 \text{ kHz} \end{aligned}$$

$$(a) \text{ CCM} \Rightarrow I_L = I_o \geq \frac{1}{2} \hat{\Delta i_L} \quad \text{--- (1)}$$

$$I_L = I_o = \frac{P_o}{V_o} = \frac{48}{12} \text{ A} = \underline{4 \text{ A}}$$

$$\text{CCM} \Rightarrow V_o = DV_d \Rightarrow D = \frac{12}{20} = \underline{0.6}$$

when the switch is on, $V_L = V_d - V_o = (20 - 12) \text{ V} = \underline{8 \text{ V}}$

$$V_L = L \frac{\Delta i_L}{\Delta t} = \frac{L \cdot \hat{\Delta i_L}}{DT_s} = \frac{L \cdot \hat{\Delta i_L} \cdot f_{sw}}{D}$$

$$\Rightarrow \hat{\Delta i_L} = \frac{D \cdot V_L}{L \cdot f_{sw}} \quad \text{--- (2)}$$

For CCM, $\frac{1}{2} \hat{\Delta i_L} \leq I_o = I_L$

$$\Rightarrow \frac{1}{2} \frac{D V_L}{L \cdot f_{sw}} \leq I_o \Rightarrow \frac{1}{L} \leq \frac{2 I_o \cdot f_{sw}}{D V_L}$$

$$\Rightarrow L \geq \frac{D V_L}{2 I_o \cdot f_{sw}}$$

$$\Rightarrow L_{\min} = \frac{D V_L}{2 I_o \cdot f_{sw}} = \frac{(0.6)(8)}{2(4)(20 \cdot 10^3)} \text{ H} = \underline{30 \mu\text{H}}$$

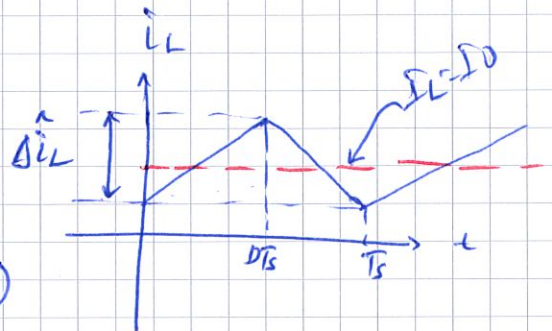
(2 pts)

(b) using results above

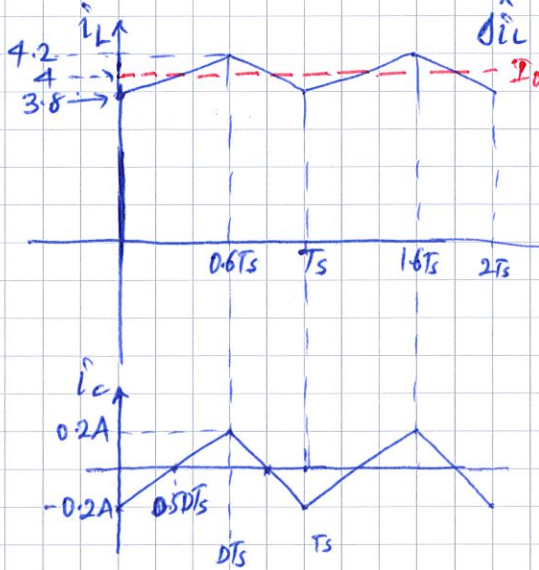
$$\hat{\Delta i_L} = \frac{D V_L}{L \cdot f_{sw}} = 0.1 I_o \Rightarrow \frac{1}{L} = \frac{0.1 I_o \cdot f_{sw}}{D V_L} = \frac{(0.1)(4)(20 \cdot 10^3)}{(0.6)(8)}$$

$$\Rightarrow L = \frac{(0.6)(8)}{(0.1)(4)(20 \cdot 10^3)} \text{ H} = \underline{0.6 \text{ mH}}$$

(2 pts)

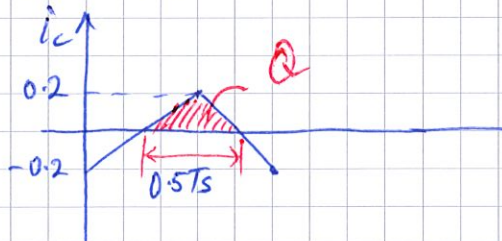


(c) $\hat{i}_L = I_0 + \hat{i}_c$; $I_0 = 4A$
 $\Delta \hat{i}_L = 0.1 I_0 = 0.4A$



(2 pts)

(d) $V_0 = 12V$, $\Delta V_0 \leq 0.01 V_0 = 0.12V$



$$\Delta V_0 = \frac{Q}{C} = \frac{\frac{1}{2} \cdot 0.5T_s \cdot \hat{i}_c}{C} = \frac{\frac{1}{2} \cdot (0.5T_s) \cdot (0.2)}{C \cdot f_{sw}} = \frac{0.05}{(20 \cdot 10^3) C}$$

$$\Delta V_0 \leq 0.12V$$

$$\Rightarrow \frac{0.05}{(20 \cdot 10^3) C} \leq 0.12$$

$$\Rightarrow C \geq \frac{0.05}{(0.12)(20 \cdot 10^3)} F$$

$$\Rightarrow C_{\min} = \frac{0.05}{(0.12)(20 \cdot 10^3)} F = \underline{\underline{20.83 \mu F}}$$

(2 pts)

CHALMERS	Anonymous code Mehtu Beza	Points for question (to be filled in by teacher)	Consecutive page no. Löpande sid nr 5
	Anonym kod	Poäng på uppgiften (fylltes av lärare)	Question no. Uppgift nr 3

$$N_1 : N_2 : N_3 = 1 : 1 : 1$$

$$V_d = 20V$$

$$f_{sw} = 20 \text{ kHz}$$

$$D = 0.3$$

$$L_m = 100 \mu H$$

$$R_{load} = 20 \Omega$$

$V_o = ?$, sketch v_{sw} , i_d , i_o

Find if the converter is in CCM or DCM mode

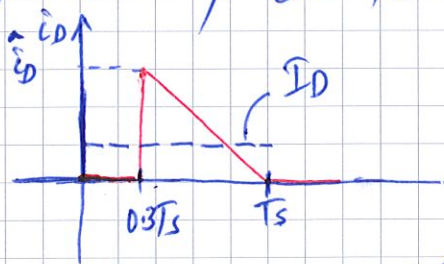
Let's start with the assumption that the converter is in CCM

$$\Rightarrow V_o = \frac{D}{1-D} \cdot V_d = \frac{0.3}{1-0.3} \cdot 20 = \underline{\underline{8.57V}} \quad (N_1 : N_2 : N_3 = 1 : 1 : 1)$$

$$\Rightarrow I_o = \frac{V_o}{R_{load}} = \frac{8.57}{20} A = \underline{\underline{0.4286A}}$$

When the switch S is off, the diode D conducts and the average diode current is the same as the output current.

at the boundary condition, we have:



$$I_D = \frac{\frac{1}{2} \hat{i}_D \cdot (1-D) T_s}{T_s} = \underline{\underline{0.35 \hat{i}_D}}$$

if $0.35 \hat{i}_D = I_o \leq I_o$, the assumption of CCM is correct.

During the switch S off period, $v_L = -\frac{N_1}{N_3} V_o = -L_m \frac{\Delta i_m}{(1-D) T_s}$ but during switch on period, $i_m = I_o$

$$\Rightarrow V_o = L_m \cdot \frac{\hat{i}_D}{0.7 T_s} = \frac{L_m (f_{sw}) \cdot \hat{i}_D}{0.7} \quad (2 \text{ pts})$$

$$\Rightarrow \hat{i}_D = \frac{(0.7)(8.57)}{100 \mu \cdot 20k} A = \underline{\underline{3A}} \Rightarrow I_D = 0.35 \hat{i}_D = \underline{\underline{1.05A}}$$

However I_D at the boundary is higher than I_o which disproves (1).

⇒ Converter operating in DCM mode

$$\Rightarrow \frac{V_o}{V_d} = D \sqrt{\frac{R T_s}{2 L_m}} \quad (\text{from energy conservation})$$

$$\Rightarrow V_o = (0.3) \sqrt{\frac{20}{2(100\mu)(20k)}} \cdot 20 \text{ V} \quad (V_o = V_d)$$

$$\Rightarrow V_o = \underline{\underline{13.42 \text{ V}}} \quad (2 \text{ pt})$$

during the switch S on period, ($i_d = i_m$, $v_{sw} = 0$, $i_o = 0$)

$$V_1 = V_d = \frac{L_m \cdot \hat{i}_m}{0 T_s} = \frac{L_m \cdot f_{sw} \cdot \hat{i}_m}{0} \quad (i_m \text{ starts from } 0 \text{ DCM})$$

$$\Rightarrow \hat{i}_m = \frac{D V_d}{L_m \cdot f_{sw}} \text{ A} = 3 \text{ A} = \hat{i}_o$$

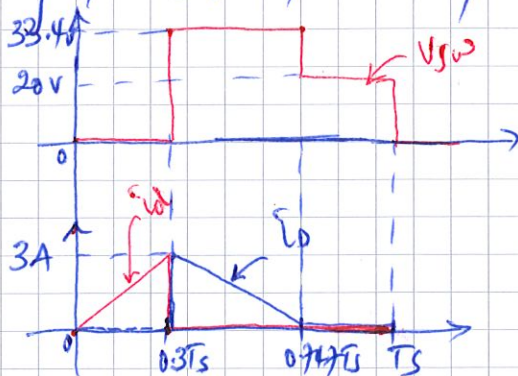
during the switch S off period, ($i_d = 0$, $i_m = i_o$, $v_{sw} = V_d + V_o$)

$$V_1 = -V_o = \frac{L_m \cdot \Delta i_m}{0_1 T_s} \Rightarrow V_o = \frac{L_m \cdot f_{sw} \cdot \hat{i}_m}{\Delta_1} \quad (\Delta_1 < (1-D))$$

$$\Rightarrow \Delta_1 = \frac{L_m \cdot f_{sw} \cdot \hat{i}_m}{V_o} = 0.447 < (1-D)$$

$$\Rightarrow I_o = (0.5 / 0.447) (3) = V_o / R_{load} \quad (\text{correct!})$$

during the discontinuous period ($i_d = 0$, $i_m = 0$, $i_o = 0$, $v_{sw} = V_d$)



(2 pt)

(a) Each switch operate for a duty cycle $D < 0.5$ in each of the half switching cycle.

$$i_L = i_{O1} + i_{O2}$$

When T_1 is on, $i_{O1} = i_L$ and $i_{O2} = 0$

When T_1 is off, $i_{O1} = i_{O2} = i_L/2$

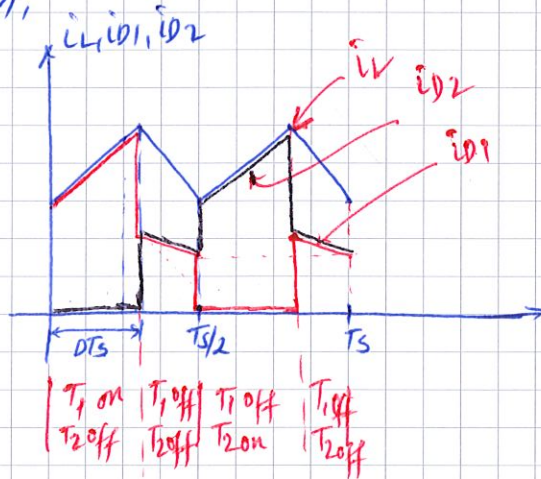
} for a period of $T_s/2$
 T_2 is off in this period

When T_2 is on, $i_{O2} = i_L$ and $i_{O1} = 0$

When T_2 is off, $i_{O2} = i_{O1} = i_L/2$

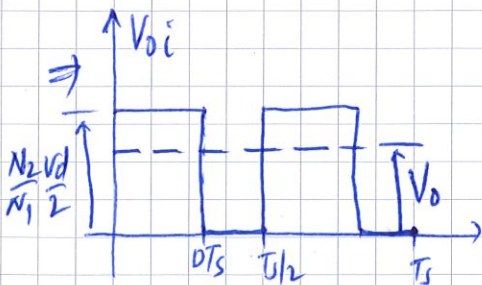
} for the next $T_s/2$
 T_1 is off in this period

For CCM,



(2 pts)

(b) $V_{O1} = \frac{N_2}{N_1} V_1$, when T_1 or T_2 are on, $V_1 = V_d/2$
when T_1 & T_2 are off, $V_1 = 0$



$$\Rightarrow V_0 = \frac{1}{T_s} \int_0^{T_s} V_{O1} dt = \frac{2 * \frac{N_2 \cdot V_d}{N_1} \cdot DT_s}{\frac{T_s}{2}} = \frac{N_2 \cdot D \cdot V_d}{N_1}$$

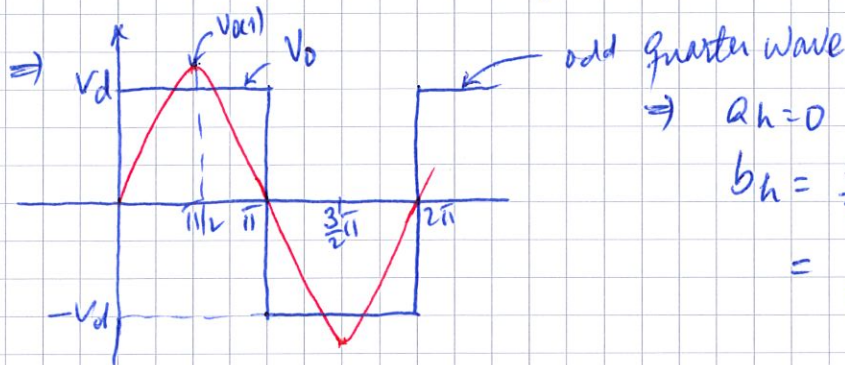
$$\Rightarrow \underline{\underline{V_0 = \frac{N_2 D}{N_1} V_d}} \quad (2 \text{ pts})$$

(c) Half bridge dc/dc converter \Rightarrow bipolar core excitation & small magnetizing current (much less than load current)

Flyback converter \Rightarrow unipolar core excitation & larger magnetizing current (in the order of load current). Hence air gap needed to avoid saturation.

(1 pt)

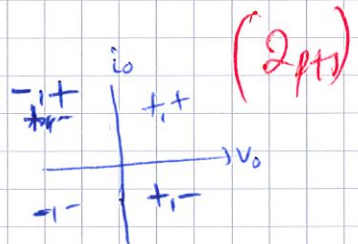
(a) $V_d = 300$, Square wave operation
 $V_o = V_d$ for half cycle of the fundamental period
 $V_o = -V_d$ for next half cycle of the fundamental period



$\Rightarrow a_h = 0$
 $b_h = \frac{4}{\pi} \int_0^{\pi/2} V_d \sin(h\theta) d\theta$ for h odd
 $= \frac{4V_d}{\pi h}$

Using Fourier calculation the peak of the fundamental component is calculated as

$\hat{V}_{o(1)} = \frac{4}{\pi} V_d = \underline{\underline{382}}$



(b) Full bridge \Rightarrow 4 Quadrant operation

v_o, i_o	+ , +	+ , -	- , -	- , +
Device conducting	S1, S2	D1, D2	S3, S4	D3, D4

(2 pts)

(c) $\hat{V}_{o(1)} = m_a \cdot V_d \Rightarrow \hat{V}_{o(1), max} = 1 \cdot 300 = \underline{\underline{300V}}$ (2 pts)

($m_a \leq 1$)

(d) Square wave: advantage: - smaller switching loss, - simpler modulation
disadvantage: - higher harmonics content, - uncontrolled output voltage
PWM: - higher fundamental output (2 pts)

PWM: advantages: - smaller low order harmonics & controlled output voltage
disadvantages: - higher switching loss, high frequency electromagnetic disturbance,

(e) advantage: - better output voltage waveform in terms of harmonics with less switching losses. disadv: - difficult control structure, more component count (2 pts)

$I_d = 20 \text{ A}$, $L_s = 0$

50 Hz , 400 V RMS, LL source

(a) $400 \text{ V, LL, RMS} \Rightarrow V_{a, \text{peak}} = \frac{400 \cdot \sqrt{2}}{\sqrt{3}} = 327$

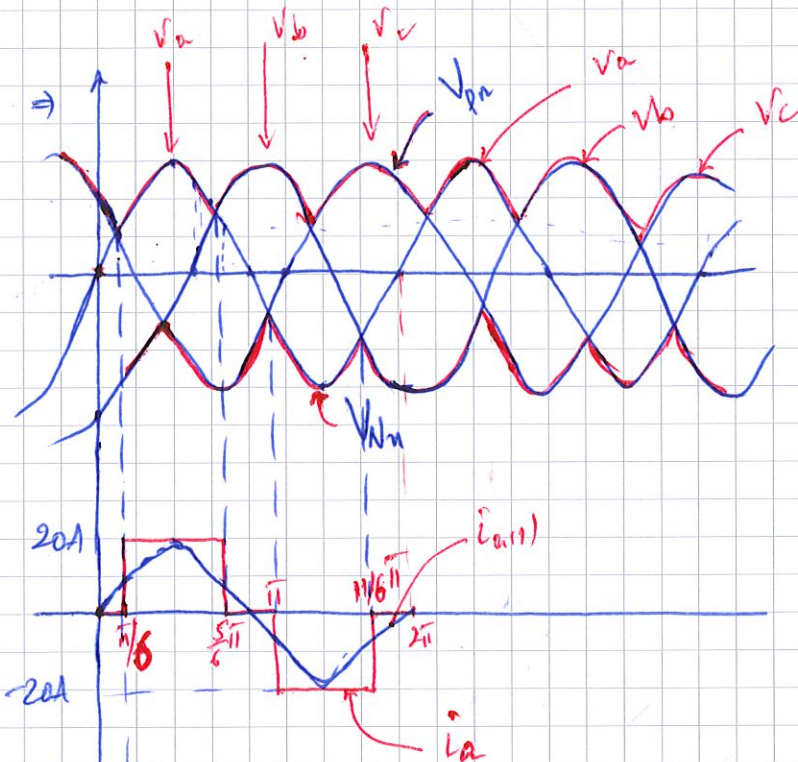
$\Rightarrow V_a = 327 \sin(\omega t)$

$V_b = 327 \sin(\omega t - 2\pi/3)$

$V_c = 327 \sin(\omega t + 2\pi/3)$

i_a flows when D_1 or D_4 is conducting. that is when V_a is higher than V_b & V_c or V_a is lower than both V_b & V_c . Similarly for i_b & i_c

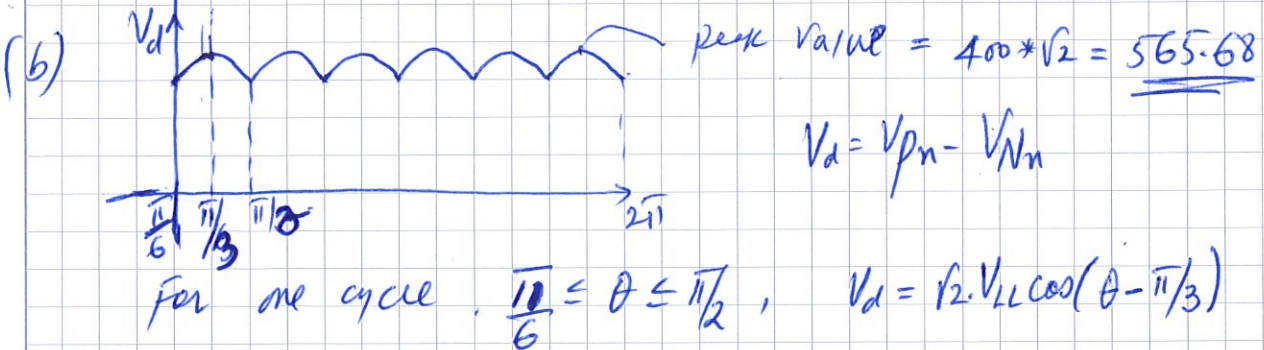
$\Rightarrow V_d = V_p - V_N = \max(V_a, V_b, V_c) - \min(V_a, V_b, V_c)$



← drawing not to scale

(2 pts)

DPF is $\frac{1}{\sqrt{3}}$. $DPF = \cos(\phi) = 1$



$$\begin{aligned}
 \Rightarrow V_d &= \frac{1}{\pi/3} \int_{\pi/6}^{\pi/2} \sqrt{2} V_{LL} \cos(\theta - \pi/3) d\theta \\
 &= \frac{3}{\pi} \int_{-\pi/6}^{\pi/6} \sqrt{2} V_{LL} \cos(\theta) d\theta \\
 &= \frac{3}{\pi} \cdot \sqrt{2} V_{LL} \sin \theta \Big|_{-\pi/6}^{\pi/6} = \frac{6\sqrt{2}}{\pi} V_{LL} \sin(\pi/6) = \frac{3\sqrt{2}}{\pi} V_{LL}
 \end{aligned}$$

$$\Rightarrow \underline{\underline{V_d = 540 \text{ V}}}$$

(2 pts)

(c) Source inductance reduces both the average output voltage and the DPF.

$$\text{DPF} = \cos(u/2) \quad \text{where } u = \text{commutation angle.}$$

(2 pts)

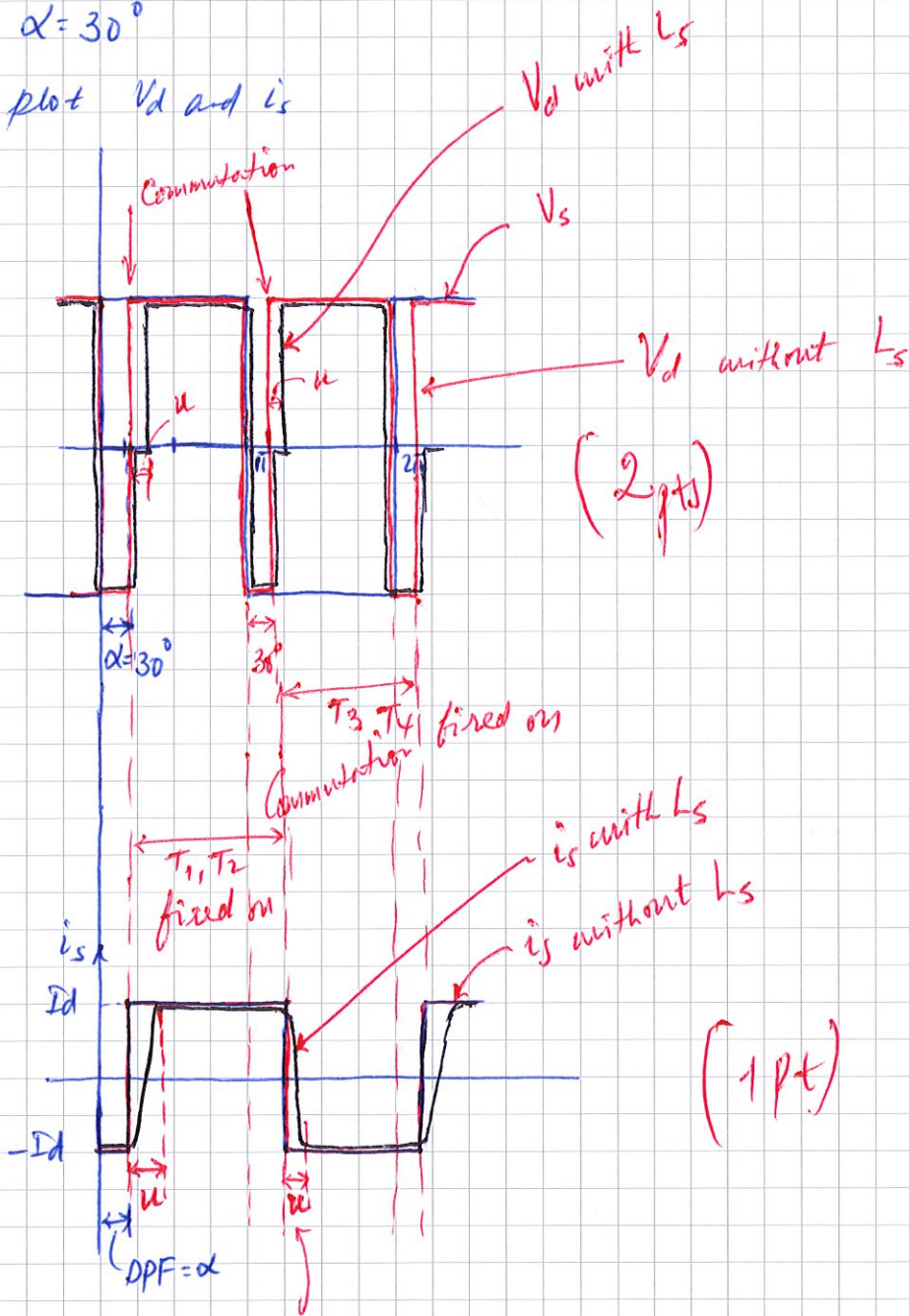
$L_s = 5mH$

$V = \pm 100V, 50Hz$

$i_d = 10A$

$\alpha = 30^\circ$

(a) plot V_d and i_s



(2 pts)

(1 pt)

during 2 commutations $V_d = 0$
 for $2 \times 30^\circ$, $V_d = -100V$
 for other intervals $V_d = 100V$

(b) Find u , as i_s increases from $-I_d$ to I_d in the interval of u , V_d is during commutation.

$$\Rightarrow V_s = L_s \frac{di_s}{dt} \quad \text{where } V_s = 100$$

$$\Rightarrow V_s = L_s \cdot \frac{di_s}{d\theta} \cdot \frac{d\theta}{dt} = L_s \frac{di_s}{d\theta} \cdot \omega = L_s \omega \cdot \frac{di_s}{d\theta}$$

$$\Rightarrow V_s d\theta = L_s \omega \cdot di_s$$

$$\Rightarrow \int_0^u V_s d\theta = L_s \omega \int_{-I_d}^{I_d} di_s$$

$$\Rightarrow 100 \cdot u = L_s \omega \cdot 2I_d$$

$$\Rightarrow u = \frac{2L_s \omega \cdot I_d}{100} \text{ rad} = \frac{2(5 \cdot 10^{-3})(2\pi \cdot 50)(10)}{100} = \underline{\underline{0.3142 \text{ rad}}}$$

$$\Rightarrow V_d = \frac{1}{2\pi} \left(2(-100)\left(\frac{\pi}{6}\right) + 2(0)(0.3142) + 2(100)(\pi - \frac{\pi}{6} - 0.3142) \right)$$

$$\Rightarrow V_d = \frac{100}{\pi} \left(\pi - \frac{2\pi}{6} - 0.3142 \right) = \underline{\underline{56.67 \text{ V}}}$$

(2 pts)

$$(a) \quad P = 3W$$

$$T_a = 25^\circ C$$

$$R_{thja} = 62^\circ C/W$$

$$T_j = T_a + P \cdot R_{thja}$$

$$= 25^\circ C + 3W \cdot 62^\circ C/W$$

(2 pts)

$$\Rightarrow \underline{\underline{T_j = 211^\circ C}}$$

the junction temperature is very high and a heat sink is needed.

$$(b) \quad T_{j,max} = 100^\circ C$$

$$\Rightarrow T_j = 25^\circ C + 3W \cdot R_{thja} \leq 100^\circ C$$

$$\Rightarrow R_{thja} \leq \frac{100^\circ C - 25^\circ C}{3W} = 25^\circ C/W$$

$$\Rightarrow \underline{\underline{R_{thja,max} = 25^\circ C/W}} \quad (2 pts)$$